

INFORMATION DISCLOSURE CITATION  
(Use several sheets if necessary)

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APPLICANT(S): PAVEL HORSKY ET AL.	
FILING DATE: March 26, 2004	GROUP ART UNIT: 2819

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JBJB	A. 2002/113723	08-22-2002	Greig David Vetea	—	—	
	B. 6,160,507	12-12-2000	Carbou Pierre et al.	—	—	
	C. 5,327,134	07-05-1994	Nakamura Yasuyuki et al.	—	—	
	D. 5,600,319	02-04-1997	Ginetti Bernard	—	—	
	E. 5,453,743	09-26-1995	Kang Ku-Chang	—	—	
JBJB	F. 5,254,994	10-19-1993	Goto Junkei et al.	—	—	
G.						
H.						
I.						

FOREIGN PATENT DOCUMENTS

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						YES		
JBJB	J. EP 0 930 717	07-21-1999	EP	—	—	X		
	K.							
	L.							
	M.							
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JBJB	Q.	Y. Nakamura et al., "A 10-bit 70 MS/s CMOS D/A Converter," IEEE 1990 Symposium on VLSI Circuits, June 7, 1990, pages 57-58.
	R.	
	S.	
	T.	
	U.	
	V.	
	W.	

EXAMINER <i>Stan Bruneau Jean Claude</i>	DATE CONSIDERED <i>3/22/05</i>
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